

```
/* Set UART configuration */
CyU3PMemSet ((uint8_t *)&uartConfig, 0, sizeof (uartConfig));
uartConfig.baudRate = CY_U3P_UART_BAUDRATE_115200;
uartConfig.stopBit = CY_U3P_UART_ONE_STOP_BIT;
uartConfig.parity = CY_U3P_UART_NO_PARITY;
uartConfig.txEnable = CyTrue;
uartConfig.rxEnable = CyFalse;
uartConfig.flowCtrl = CyFalse;
uartConfig.isDma = CyTrue;
```

```
}
```

```
    if (status != CY_U3P_SUCCESS)
{
    return;
```

```
    0x05,
CY_U3P_BOS_DESCR,
0x16,0x00,
0x02,

/* USB 2.0 extension */
0x07,
CY_U3P_DEVICE_CAPB_DESCR,
CY_U3P_USB2_EXTN_CAPB_TYPE,
0x02,0x00,0x00,0x00,

/* SuperSpeed device capability
0x0A,
CY_U3P_DEVICE_CAPB_DESCR,
CY_U3P_SS_USB_CAPB_TYPE,
```