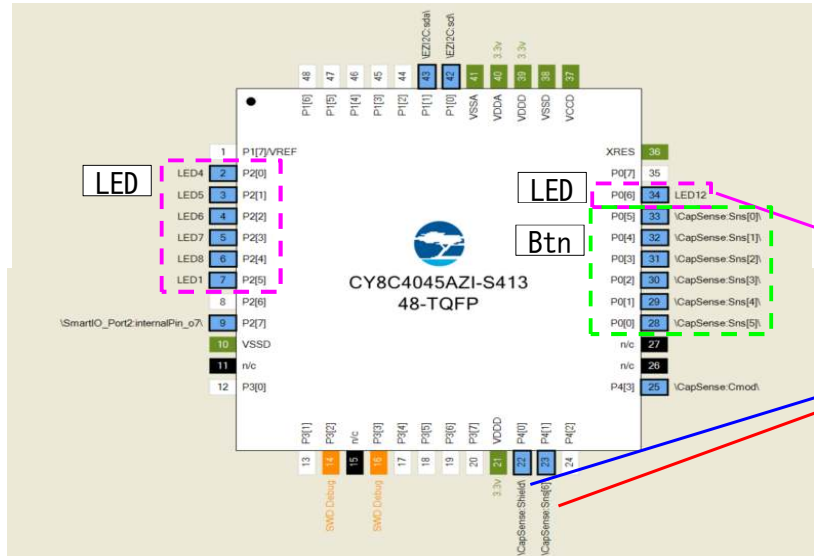
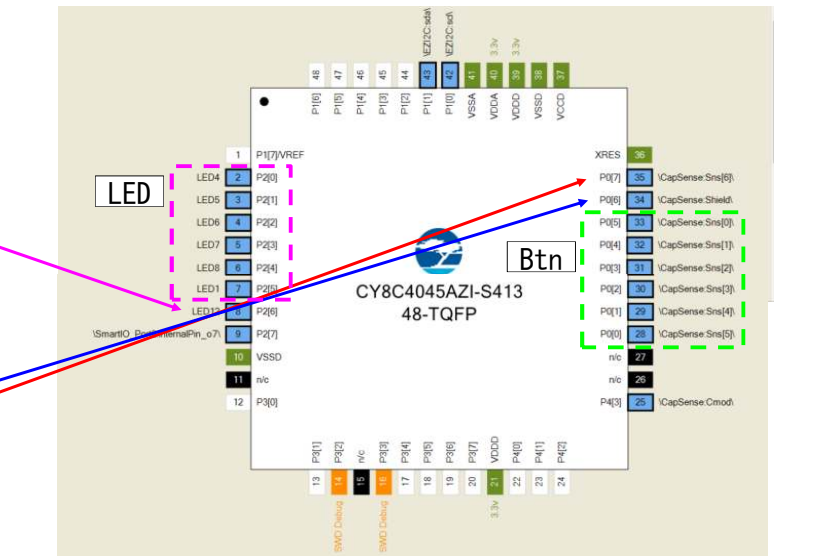


CapSense Pin Assignment

A



B



Name	Port	Pin
\CapSense:Cmod\ (Cmod)	P4[3]	25
\CapSense:Shield\ (Shield)	P4[0]	22
\CapSense:Sns[0]\ (GBTN1_Sns0)	P0[5]	33
\CapSense:Sns[1]\ (GBTN2_Sns0)	P0[4]	32
\CapSense:Sns[2]\ (GBTN3_Sns0)	P0[3]	31
\CapSense:Sns[3]\ (GBTN4_Sns0)	P0[2]	30
\CapSense:Sns[4]\ (GBTN5_Sns0)	P0[1]	29
\CapSense:Sns[5]\ (GBTN6_Sns0)	P0[0]	28
\CapSense:Sns[6]\ (GUARD_Sns0)	P4[1]	23
\EZI2C:scl\	P1[0]	42
\EZI2C:sda\	P1[1]	43
\SmartIO_Port2:internalPin_o7\	P2[7]	9
LED1	P2[5]	7
LED4	P2[0]	2
LED5	P2[1]	3
LED6	P2[2]	4
LED7	P2[3]	5
LED8	P2[4]	6
LED12	P0[6]	34

Name	Port	Pin
\CapSense:Cmod\ (Cmod)	P4[3]	25
\CapSense:Shield\ (Shield)	P0[6]	34
\CapSense:Sns[0]\ (GBTN1_Sns0)	P0[5]	33
\CapSense:Sns[1]\ (GBTN2_Sns0)	P0[4]	32
\CapSense:Sns[2]\ (GBTN3_Sns0)	P0[3]	31
\CapSense:Sns[3]\ (GBTN4_Sns0)	P0[2]	30
\CapSense:Sns[4]\ (GBTN5_Sns0)	P0[1]	29
\CapSense:Sns[5]\ (GBTN6_Sns0)	P0[0]	28
\CapSense:Sns[6]\ (GUARD_Sns0)	P0[7]	35
\EZI2C:scl\	P1[0]	42
\EZI2C:sda\	P1[1]	43
\SmartIO_Port2:internalPin_o7\	P2[7]	9
LED1	P2[5]	7
LED4	P2[0]	2
LED5	P2[1]	3
LED6	P2[2]	4
LED7	P2[3]	5
LED8	P2[4]	6
LED12	P2[6]	8