Creating and Modifying User Modules And other Unnatural Acts

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Objectives

Objective:

- Introductive the fundamental components of a PSoC User Module.
- Show how XML is used to describe the intraconnection of the PSoC recourses.
- Create an improved UM (timer16X)using the old UM (timer16) as a template.



A User Module is:

- The information required to connect PSoC blocks.
- Software (API) to control it
- A Datasheet
- An Icon



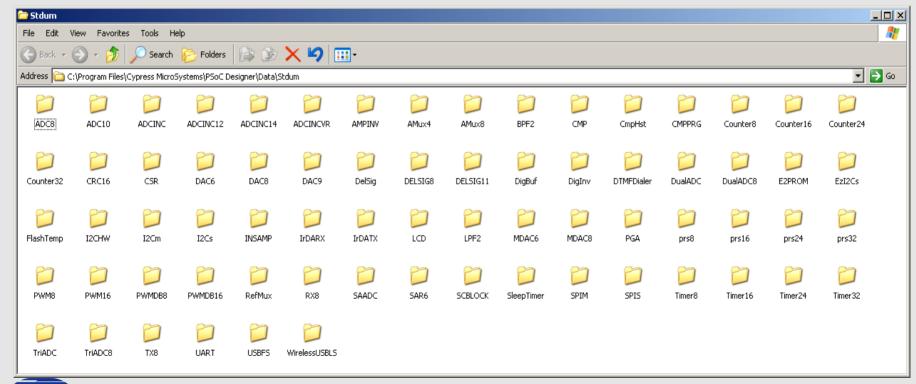
User Module

- At its most basic form is a directory and an XML file with the same name.
 - The same name must also appear as the NAME attribute in the <PSOC_USER_MODULE> element in the UM XML file.
- If done correctly the UM is added to the active UM library.
- Other files exist in the User Module directory, but their naming is controlled by attributes within the UM XML file.
- The other files which are relevant to the UM
 - *.ico User Module icon
 - *.htm User Module data sheet
 - *.emf User Module block diagram



User Module Directory

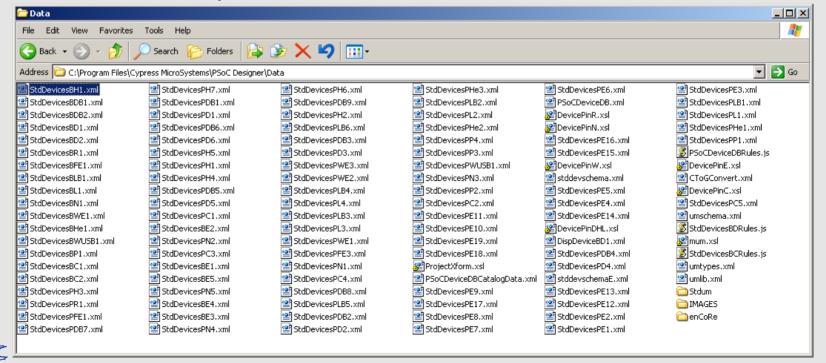
 All UMs located at C:\Program Files\Cypress MicroSystems\PSoC Designer\Data\Stdum





User Module Directory

- All device descriptions located at C:\Program Files\Cypress MicroSystems\PSoC Designer\Data
 - Think of each as a dictionary of acceptable register and bit field values for each particular device.



Making New User Modules:

- Building a UM from scratch is extremely difficult to do.
- It is far easier to start with a copy of an established UM and modify it to meet your needs.
- Information from several different UMs can be combined to produce a new UM.
 - The counter8 XML and the DAC6 XML could be combined to produce a waveform generator UM.
 - Think of it as gene splicing.



Project Goal

Build an improved Timer16

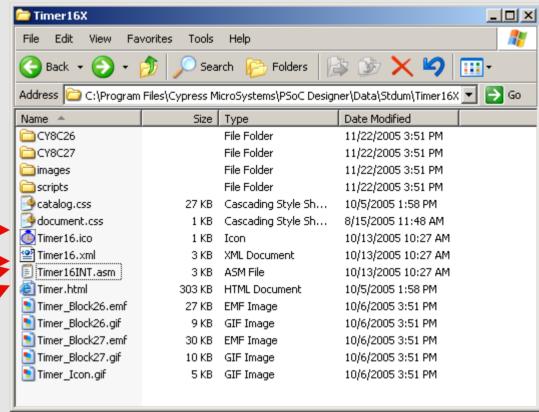
- The upper digital block of the present Timer16 has a parameter selection to interrupt on:
 - Capture
 - Terminal Count
 - Compare True
- The goal is to create a new UM (timer16X) that:
 - Generates a Terminal Count interrupt for the upper digital block.
 - Generates a Capture interrupt for the lower digital block.



Step 1

- Make a copy of Timer16.
- Rename it Timer16X
- Open it

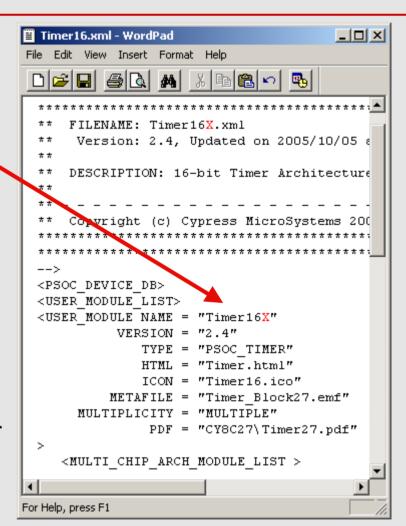






Step 2

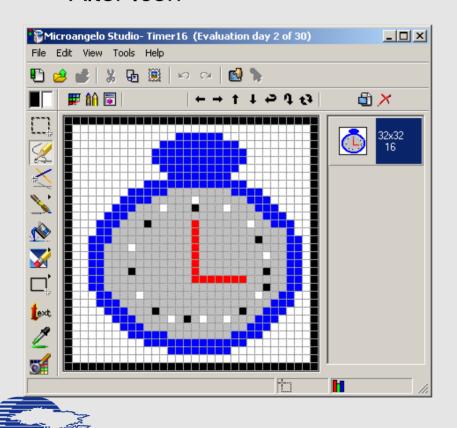
- Open Timer16.xml
 - Use your preferred text editor
- Change USER MODULE NAME. to **TimerX**.
- Might as well change the FILENAME: while you're here.
 - Its only a comment but it can't hurt.
- Close the file and reopen with Internet Explorer.
 - Should open with no problems.
 - This checking process will be done frequently.
 - Lets call it CRET

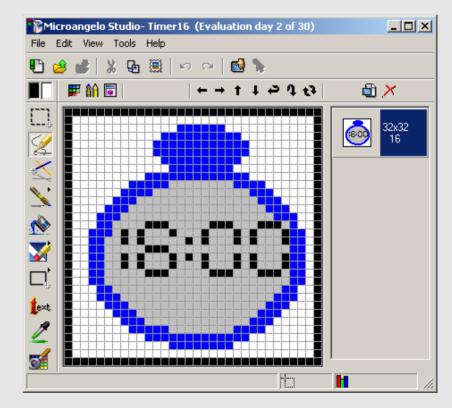


Close, Reopen with Explorer to Test

Step 3

- Open Timer16.ico
- Alter Icon





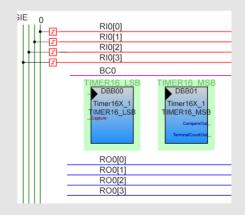
Sanity Check! Open a 27x project with Designer

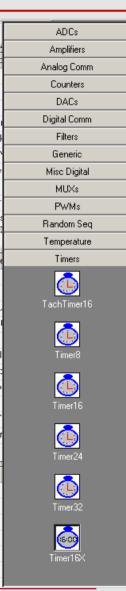
- Verify that UM is present.
- Verify that it places.

For many UMs this is all the XML changes that are required.

- For UMs that have significant changes in software but no hardware changes.
 - This is your chance to change the APIs of your favorite UM to the way that you (and God) think they should be, while leaving the base UM hardware configuration alone!

User Module Parameters	Value
Clock	?
Capture	?
TerminalCountOut	?
CompareOut	?
Period	0
CompareValue	0
CompareType	?
InterruptType	?
ClockSync	?
TC_PulseWidth	Full Clock
InvertCapture	Normal





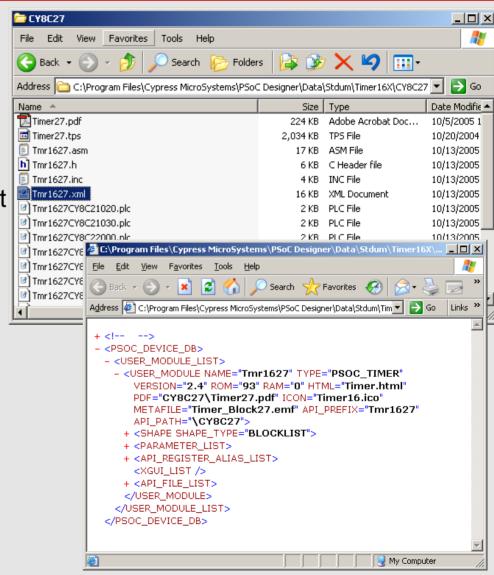


Open CY8C27

Open Tmr1627.xml

XML has 4 main sections

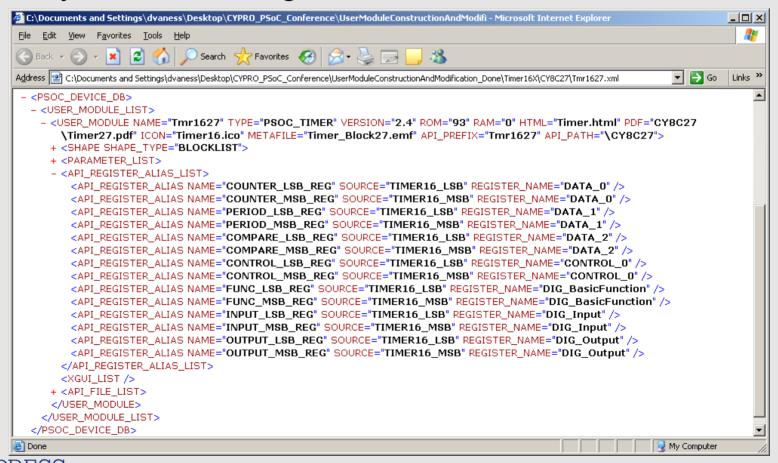
- SHAPE
 - For bit fields are set that don't change.
- PARAMETER LIST
 - For bit fields are set by user selection.
- API REGISTER ALIS LIST
 - Allows appropriate names of registers.
- API FILE LIST
 - Lists the API files to be generated.





API_REGISTER_ALIAS_LIST

Allows you to define register names.



API_FILE

- Allow you to define name and location of all API files.
 - Is possible to add multiple assembly files
 - Add C routines.

```
C:\Documents and Settings\dvaness\Desktop\CYPRO_PSoC_Conference\UserModuleConstructionAndModifi - Microsoft Internet Explorer
                                                                                                                                  File Edit View Favorites Tools Help
                                 Search 🌟 Favorites 🚱 🛜 🖈 🔙 🤜
                                                                                                                        ▼ 🗦 Go
Address 🎱 C:\Documents and Settings\dvaness\Desktop\CYPRO_PSoC_Conference\UserModuleConstructionAndModification_Done\Timer16X\CY8C27\Tmr1627.xml
                                                                                                                                  Links "
 - <PSOC_DEVICE_DB>
   - <USER MODULE LIST>
    - <USER MODULE NAME="Tmr1627" TYPE="PSOC TIMER" VERSION="2.4" ROM="93" RAM="0" HTML="Timer.html" PDF="CY8C27"
        \Timer27.pdf" ICON="Timer16.ico" METAFILE="Timer Block27.emf" API PREFIX="Tmr1627" API PATH="\CY8C27">
      + <SHAPE SHAPE_TYPE="BLOCKLIST">
      + <PARAMETER LIST>
      + <API REGISTER ALIAS LIST>
        <XGUI LIST />
      - <API_FILE_LIST>
          <API FILE NAME="Timer16INT.asm" API PREFIX="Timer16" API PATH="\" />
          <API FILE NAME="Tmr1627.inc" />
          <API FILE NAME="Tmr1627.asm" />
          <API_FILE NAME="Tmr1627.h" />
        </API_FILE_LIST>
      </USER MODULE>
     </USER MODULE LIST>
   </PSOC_DEVICE_DB>
                                                                                                                    My Computer
Done
```



To modify this UM

- Lower Block must be set to also be an interrupt source in the SHAPE section.
- The parameter used to select interrupt type must be removed from the PARAMETER_LIST area.
- Bit Values must be added to the SHAPE section to set the interrupt type to:
 - Terminal Count for the Upper Block
 - Capture for the Lower Block

Step 4

This user module will not work for 25x and 26x family of parts. So

Open Timer16X.xml
 Remove

CRET

- The CY8C26 directory is no longer needed.
 - Remove it!

```
Timer16X.xml - WordPad
                                                                    _ | U | X |
File Edit View Insert Format Help
 X Ba Ca or Ca
 <PSOC DEVICE DB>
 <USER MODULE LIST>
 <USER MODULE NAME = "Timer16X"</pre>
           VERSION = "2.4"
               TYPE = "PSOC TIMER"
               HTML = "Timer.html"
               ICON = "Timer16.ico"
          METAFILE = "Timer Block27.emf"
      MULTIPLICITY = "MULTIPLE"
                PDF = "CY8C27\Timer27.pdf"
    <MULTI CHIP ARCH MODULE LIST >
        <MubT! CHIP ARCH MODULE NAME = "Tmr1626" PATH = "\CY8C26" >
           <PSOC DEVICE SPEC LIST>
              <PSOC DEVICE SPEC NAME = "CY8C25000" />
              <PSOC DEVICE SPEC NAME = "CY8CZ6899" />
           FSOC DEVICE SPEC LIST>
        </MULTI CHIP ARCH MODULE>
        <MULTI CHIP ARCH MODULE NAME = "Tmr1627" PATH = "\CY8C27"</pre>
          <PSOC DEVICE SPEC LIST>
              <PSOC DEVICE SPEC NAME = "CY8C21020" />
              <PSOC DEVICE SPEC NAME = "CYWUSB6900" />
              <PSOC DEVICE SPEC NAME = "CY8C21030" />
              <PSOC DEVICE SPEC NAME = "CY8C22000" />
              <PSOC DEVICE SPEC NAME = "CY8C22000B" />
              <PSOC DEVICE SPEC NAME = "CY8C24000" />
              <PSOC DEVICE SPEC NAME = "CY8C24090" />
              <PSOC DEVICE SPEC NAME = "CY8C24000B" />
              <PSOC DEVICE SPEC NAME = "CY8C27000" />
              <PSOC DEVICE SPEC NAME = "CY8C27000B" />
              <PSOC DEVICE SPEC NAME = "CY8C27060" />
              <PSOC DEVICE SPEC NAME = "CY8C27100" />
              <PSOC DEVICE SPEC NAME = "CY8C27100B" />
              <PSOC DEVICE SPEC NAME = "CY8C29000" />
           </PSOC DEVICE SPEC LIST>
        </MULTI CHIP ARCH MODULE>
    </MULTI CHIP ARCH MODULE LIST >
 </USER MODULE>
 </user Module List>
For Help, press F1
```



Step 5 Add Interrupt

- Open Tmr1627.xml
- Use interrupt code from TIMER16_MSB block to add an interrupt to Timer16LSB block.
 - Name this new interrupt source _CaptureISR

CRET

```
Tmr1627.xml - WordPad
                                                                           File Edit View Insert Format Help
     <SHAPE SHAPE TYPE = "BLOCKLIST">
        <BLOCK LIST>
           <BLOCK
                               NAME = "TIMER16 LSB"
                               TYPE = "DIGITAL"
                  INTERRUPT SOURCE = " CaptureISR"
                    INTERRUPT TYPE = "JUMP"
              <REGISTER LIST>
                 <REGISTER NAME = "DIG BasicFunction">
                    <BITFIELD LIST>
                        <BITFIELD NAME = "Function"
                                                        VALUE = "Timer" />
                        <BITFIELD NAME = "End"
                                                        VALUE = "NotEnd" />
                    </BITFIELD LIST>
                 </REGISTER>
              </REGISTER LIST>
              <INPUT LIST/>
           </BLOCK>
           <BLOCK
                               NAME = "TIMER16 MSB"
                               TYPE = "DIGITAL"
                  INTERRUPT SOURCE = " ISR"
                    INTERRUPT TYPE = "JUMP"
                 <REGISTER NAME = "DIG BasicFunction">
                    <BITFIELD LIST>
                        <BITFIELD NAME = "Function"
                                                        VALUE = "Timer" />
                        <BITFIELD NAME = "End"
                                                        VALUE = "IsEnd" />
                    </BITFIELD LIST>
                 </REGISTER>
              </REGISTER LIST>
              <INPUT LIST>
                 <INPUT
                                SOURCE = "TIMER16 LSB"
                                  TYPE = "BLOCK"
                        REGISTER NAME = "DIG Input"
                              BITFIELD = "DataSelect"
                 />
              </INPUT LIST>
           </BLOCK>
        </BLOCK LIST>
     </SHAPE>
For Help, press F1
```



Sanity Check! Open a 27x project with Designer

- Place Timer16X
- Generate application.
- Go to application editor
 - Open boot.asm
- Verify that the new interrupt vector has been added.

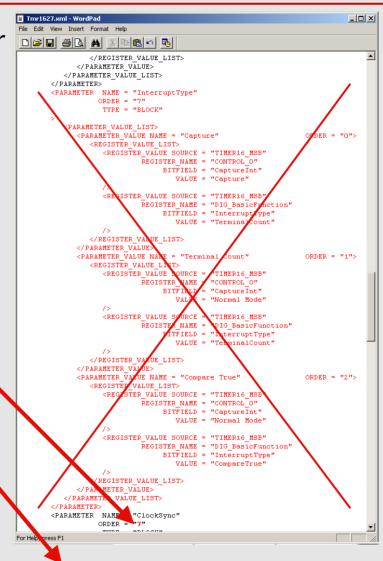
```
🔼 boot.asm
                                                                       _ | D | X
      // call void handler
      reti
                                      ;GPIO Interrupt Vector
            1Ch
      // call void handler
                                      ;PSoC Block DBBOO Interrupt Vector
              Timer16X 1 CaptureISR
                                      :PSoC Block DBB01 Interrupt Vector
              Timer16X 1 ISR
      1 imp
                                      ;PSoC Block DCBO2 Interrupt Vector
      // call void handler
            2Ch
                                      :PSoC Block DCB03 Interrupt Vector
      // call void handler
            30h
                                      ;PSoC Block DBB10 Interrupt Vector
      // call void handler
                                      ;PSoC Block DBB11 Interrupt Vector
           34h
      // call void handler
```



Step 6 Remove Interrupt Type Parameter

- Open Tmr1627.xml
- Remove the InterruptType parameter.
 - Save removed portion in a temporary file.
- For ClockSync parameter
 - Change order from 8 to 7
- For TC_Width parameter
 - Change order to from 9 to 8

CRET





Step 7 Add Interrupts to SHAPE Open temporary file.

The REGISTER_NAME,
 BITFIELD, and VALUE for each
 type of interrupt can be used to
 define the personalize the
 requires registers.

```
New Text Document.txt - WordPad
                                                                                 _ | 🗆 | × |
   Edit View Insert Format Help
              <PARAMETER VALUE NAME = "Capture"
                                                                      ORDER = "O">
                  <REGISTER VALUE LIST>
                     <REGISTER VALUE SOURCE = "TIMER16 MSB"
                              REGISTER NAME = "CONTROL O"
                                   BITFIELD = "CaptureInt"
                                      VALUE = "Capture"
                     <REGISTER VALUE SOURCE = "TIMER16 MSB"
                              REGISTER NAME = "DIG BasicFunction"
                                   BITFIELD = "InterruptType"
                                      VALUE = "TerminalCount"
                 </REGISTER VALUE LIST>
              </PARAMETER VALUE>
              <PARAMETER VALUE NAME = "Terminal Count"
                                                                      ORDER = "1"
                  <REGISTER VALUE LIST>
                     <REGISTER VALUE SOURCE = "TIMER16 MSB"
                              REGISTER NAME = "CONTROL O"
                                   BITFIELD = "CaptureInt"
                                      VALUE = "Normal Mode"
                     <REGISTER VALUE SOURCE = "TIMER16 MSB"
                              REGISTER NAME = "DIG BasicFunction"
                                   BITFIELD = "InterruptType"
                                       VALUE = "TerminalCount"
For Help, press F1
```



Step 7b Add Interrupts to SHAPE

Open Tmr1627.XML

For TIMER16_LSB

- Add "InterruptType" bit field to the "DIG_BasicFunction" register.
 - Set its value to "TerminalCount"
- Add CONTROL_0 register
- Add "CaptureInt" bit field to it.
 - Set its value to "Capture".

```
Tmr1627.xml - WordPad
                                                                                      _ | D | X |
File Edit View Insert Format Help
     <SHAPE SHAPE TYPE = "BLOCKLIST">
        <BLOCK LIST>
           <BLOCK
                               NAME = "TIMER16 LSB"
                               TYPE = "DIGITAL"
                  INTERRUPT SOURCE = " CaptureISR"
                    INTERRUPT TYPE = "JUMP"
               <REGISTER LIST>
                 <REGISTER NAME = "DIG BasicFunction">
                    <BITFIELD LIST>
                        <BITFIELD NAME = "Function"
                                                        VALUE = "Timer" />
                        <BITFIELD NAME = "InterruptType" VALUE = "TerminalCount"/>
                        <BITTIELD NAME = "End"
                                                        VALUE = "NotEnd" />
                    </BITFIELD LIST>
                  </REGISTER>
                  <REGISTER NAME = "CONTROL 0">
                    <BITFIELD LIST>
                        <BITFIELD NAME = "CaptureInt" VALUE = "Capture" />
                    </BITFIELD LIST>
                 </REGISTER>
               </REGISTER LIST>
              <INPUT LIST/>
           </BLOCK>
           <BLOCK
                               NAME = "TIMER16 MSB"
                               TYPE = "DIGITAL"
                  INTERRUPT SOURCE = " ISR"
                    INTERRUPT TYPE = "JUMP"
               <REGISTER LIST>
                 <REGISTER NAME = "DIG BasicFunction">
                    <BITFIELD LIST>
                        <BITFIELD NAME = "Function"
                                                        VALUE = "Timer" />
                        <BITFIELD NAME = "End"
                                                        VALUE = "IsEnd" />
                        <BITFIELD NAME = "InterruptType" VALUE = "TerminalCount" />
                    </BITFIELD LIST>
                 </REGISTER>
                 <REGISTER NAME = "CONTROL O">
                    <BITFIELD LIST>
                        <BITFIELD NAME = "CaptureInt" VALUE = "Normal Mode" />
                    </BITFIELD LIST>
                 </REGISTER>
              </REGISTER LIST>
               <INPUT LIST>
                 <INPUT
                                SOURCE = "TIMER16 LSB"
                                  TYPE = "BLOCK"
For Help, press F1
```



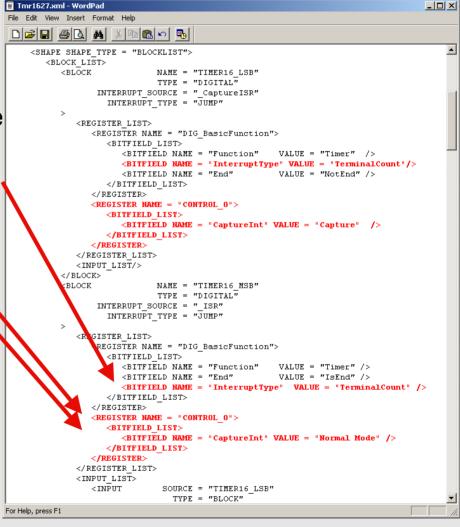
Step 7c Add Interrupts to SHAPE

For TIMER16_MSB

- Add "InterruptType" bit field to the "DIG_BasicFunction" register.
 - Set its value to "TerminalCount"
- Add CONTROL_0 register
- Add "CaptureInt" bit field to it.
 - Set its value to "Normal Mode"

CRET

All XML changes are done!





To modify software for this UM

- A new interrupt handler must be added for the new interrupt.
- API must be changed to pass the intended interrupt to enable or disable type.
- An new interrupt mask must be generated for this interrupt.
 - For both C (.h) and assembly (.inc)
- Placement file must be change for 29x parts so both interrupt masks are always in the same interrupt register.

Step 8 Add interrupt handler.

- Back up a directory
- Open Timer16INT.asm
- Duplicate the _ISR function and rename to _CaptureISR
- Add export for new Label

```
Timer16INT.asm - WordPad
                                                                        _ | 🗆 | × |
File Edit View Insert Format Help
    FUNCTION NAME: _ '@INSTANCE_NAME'_ISR
    DESCRIPTION: Unless modified, this implements only a null handler stu
  '@INSTANCE NAME' ISR:
     ;@PSoC UserCode BODY@ (Do not change this line.)
         NOTE: interrupt service routines must preserve
        the values of the A and X CPU registers.
     ; Insert your custom code above this banner
     ;@PSoC UserCode END@ (Do not change this line.)
    FUNCTION NAME: _ `@INSTANCE_NAME` CaptureISR
    DESCRIPTION: Unless modified, this implements only a null handler stu
   '@INSTANCE NAME' CaptureISR:
     ;@PSoC UserCode BODY@ (Do not change this line.)
        NOTE: interrupt service routines must preserve
         the values of the A and X CPU registers.
     ;@PSoC UserCode END@ (Do not change this line.)
  ; end of file `@INSTANCE NAME`INT.asm
For Help, press F1
```

Step 9 Change APIs

- Open CY8C27
- Open Tmr1627.asm

Both APIs are macros. These will both changed when Tmr1627.inc is changed.

Although not needed for this particular example. This step is still shown. Many UMs you develop will require some assembly code alteration.

```
Tmr1627.asm - WordPad
                                                                           : FUNCTION NAME: 'RINSTANCE NAME' EnableInt
 : DESCRIPTION:
       Enables this timer's interrupt by setting the interrupt enable mask bit
       associated with this User Module. This function has no effect until and
       unless the global interrupts are enabled (for example by using the
       macro MSC EnableGInt).
    ADCHMENTS .
                  Mone
    DETHIDNS.
                  Nothing.
      The A and X registers may be modified by this or future implementations
      of this function. The same is true for all RAM page pointer registers in
      the Large Memory Model. When necessary, it is the calling function's
      responsibility to perserve their values across calls to fastcall16
   '@INSTANCE NAME' EnableInt:
   @INSTANCE NAME EnableInt:
    RAM PROLOGUE RAM USE CLASS 1
     '@INSTANCE_NAME'_EnableInt_M
    RAM EPILOGUE RAM USE CLASS 1
  .ENDSECTION
For Help, press F1
```

```
Tmr1627.asm - WordPad
                                                                                _ | U ×
 ; FUNCTION NAME: '@INSTANCE NAME' DisableInt
       Disables this timer's interrunt by clearing the interrunt enable
       mask bit associated with this User Module.
                   Nothing
    SIDE EFFECTS:
      The {\tt A} and {\tt X} registers may be modified by this or future implementations
      of this function. The same is true for all RAM page pointer registers in
      the Large Memory Model. When necessary, it is the calling function's
      responsibility to perserve their values across calls to fastcall16
   '@INSTANCE NAME' DisableInt:
   '@INSTANCE NAME' DisableInt:
    RAM PROLOGUE RAM USE CLASS 1
     '@INSTANCE NAME' DisableInt M
    RAM EPILOGUE RAM USE CLASS 1
 ENDSECTION
  SECTION
For Help, press F1
```



Step 10 Alter include file.

- Open Tmr1627.inc
- Duplicate the _INT_ MASK declaration and change for new _CaptureINT_MASK declaration.
- Change enable and disable interrupt macros to use A as the enabling and disabling mask.

```
Tmr1627.inc - WordPad
                                                                               _ I I X
File Edit View Insert Format Help
 Version: 2.4, Updated on 2005/10/05 at 10:25:03
     'MPSOC VERSION'
     DESCRIPTION: Assembler declarations for the Timer16 user module interface
                for the 22/24/27/29xxx PSoC family of devices
 include "m8c.inc"
  @INSTANCE NAME: CONTROL REG START BIT: '@CTAB44'equ 0x01
                                                       ; Control register start bit
  @INSTANCE NAME INT REG:
                                      @INSTANCE NAME INT MASK:
                                      '@CTAB44'equ Ox'@TIMER16 MSB ISR MASK'
  '@INSTANCE NAME' CaptureINT MASK:
For Help, press F1
Tmr1627.inc - WordPad
                                                                        _ | D | X |
File Edit View Insert Format Help
 macro '@INSTANCE NAME' EnableInt M
    ; MSC EnableIntMask `@INSTANCE_NAME`_INT_REG, `@INSTANCE_NAME`_INT_MASK
    push A
    mov A, reg['@INSTANCE NAME' INT REG]
         reg['@INSTANCE NAME' INT REG],A
    endm
    macro '@INSTANCE NAME' DisableInt M
    :MSC DisableIntMask '@INSTANCE NAME' INT REG, '@INSTANCE NAME' INT MASK
    mov
    cpl A
    push A
    mov A,reg['@INSTANCE NAME' INT REG]
        reg['@INSTANCE NAME' INT REG],A
    pop
    endm
For Help, press F1
```



Step 11 Alter .h file.

- Open Tmr1627.h
- Change EnableInt and DisableInt function prototypes to require a BYTE argument.
- Duplicate the _INT_ MASK #define and change for new _CaptureINT_MASK define

```
Tmr1627.h - WordPad
                                                                                              _ | _ | × |
File Edit View Insert Format Help
 // Prototypes of the '@INSTANCE NAME' API.
  //extern void `@INSTANCE NAME` EnableInt(void);
                                                                             // Proxv 1
  extern void `@INSTANCE NAME` EnableInt(BYTE bMask);
                                                                            // Proxy 1
 //extern void `@INSTANCE NAME` DisableInt(void);
                                                                             // Proxy 1
  extern void `@INSTANCE NAME` DisableInt(BYTE bMask);
                                                                            // Proxy 1
 extern void '@INSTANCE NAME' Start (void);
                                                                           // Proxy 1
  extern void `@INSTANCE NAME` Stop(void);
                                                                           // Proxy 1
 extern WORD '@INSTANCE_NAME'_wReadTimer(void);
                                                                            // Proxy 1
  extern WORD '@INSTANCE NAME' wReadTimerSaveCV(void);
                                                                            // Proxv 2
  extern void '@INSTANCE NAME' WritePeriod(WORD wPeriod);
                                                                            // Proxv 1
 extern WORD '@INSTANCE NAME' wReadCompareValue(void);
                                                                            // Proxy 1
 extern void '@INSTANCE NAME' WriteCompareValue(WORD wCompareValue);
                                                                           // Proxv 1
 // The following functions are deprecated.
 // They may be omitted in future releases
 extern WORD w'@INSTANCE NAME' ReadCompareValue(void);
                                                               // Deprecated
  extern WORD w'@INSTANCE NAME' ReadTimerSaveCV(void);
                                                               // Deprecated
  extern WORD w'@INSTANCE_NAME`_ReadCounter(void);
                                                               // Obsolete
  extern WORD w'@INSTANCE NAME' ReadTimer(void);
                                                               // Deprecated
 extern WORD w'@INSTANCE NAME' CaptureCounter(void);
                                                               // Obsolete
 // Constants for '@INSTANCE NAME' API's.
  #define '@INSTANCE NAME' CONTROL REG START BIT '@CTAB48'( 0x01 )
 #define `@INSTANCE_NAME`_INT_REG_ADDR
                                                  '@CTAB48' ( Ox'@TIMER16 MSB ISR ADDR' )
  #define `@INSTANCE NAME` INT MASK
                                                  '@CTAB48'( Ox'@TIMER16 MSB ISR MASK')
 #define '@INSTANCE NAME' CaptureINT MASK
                                                  '@CTAB48'( Ox'@TIMER16 LSB CaptureISR MASK' )
 // Constants for '@INSTANCE NAME' user defined values
For Help, press F1
```



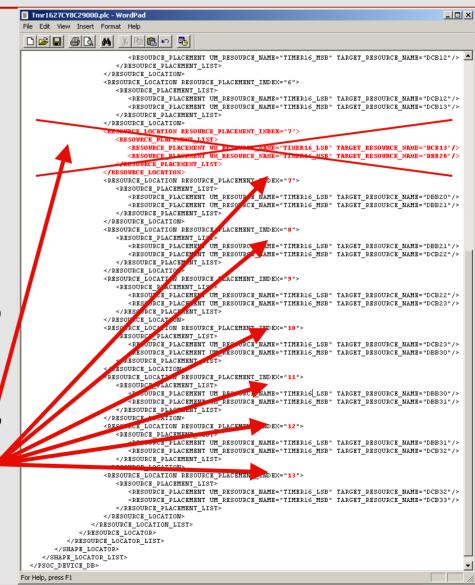
Step 12

Alter 29x placement file

Placement files list all legal placements for a particular chip family.

Only the 29x family allows the placement so the each block has a different interrupt register. DCB13 DBB20

- Open Tmr1627CY8C2900.plc
- Remove **RESOURCE_PLACEMENT_INDEX="7"**
- Renumber all following indexes.





Final Step.

- Start up Designer for a 29x part
- Verify the correct placement combinations.
- Generate application
- Verify that API compile correctly



Summary

- Each User Module is a combination of information on the intraconnections of PSoC resources, the software to control it, an icon, and a data sheet.
- It is possible to generate new or modify existing ones
- Different UMs can be combined to produce a new UM
- New UMs can be old ones with no change to the hardware. Only changes to the your custom APIs.



Questions

