

QDRII SRAM PART NUMBER DECODER

Character	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Example Part Number	C	Y	7	C	1	3	1	2	B	V	1	8	-	2	0	0	B	Z	X	C	

Character	Value	Description				
0-3		CYPRESS SRAMs				
	Digit 4:7					Digit 8
		Width	Burst Mode	Latency	Density	
	1292	x18	B2	1.5	9M	D: 90nm
	1294	x36	B2	1.5	9M	D: 90nm
	1310	x8	B2	1.5	18M	A: 150nm; B: 90nm errata affected part C: 90nm errata free J: 300MHz K: 65nm
	1311	x8	B4	1.5	18M	A: 150nm B: 90nm errata affected C: 90nm errata free J: 300MHz K: 65nm
	1312	x18	B2	1.5	18M	A: 150 nm, errata fix PCN040026 B: 90nm errata affected C: 90nm errata free, PCN061213 J: 300MHz K: 65nm
	1313	x18	B4	1.5	18M	A: 150nm, package change PCN040013 B: 90nm C: 90nm+errata fix, PCN61328 J: 300MHz K: 65nm

1314	x36	B2	1.5	18M	A: 150 nm, errata fix, PCN040026
					B: 90nm
					C: 90nm, errata fix, PCN061213
					J: 300MHz
					K: 65nm
1315	x36	B4	1.5	18M	A: 150nm, package change, PCN040023
					B: 90nm
					C: 90nm+errata fix PCN61328
					J: 300MHz
					K: 65nm
1411	x8	B4	1.5	36M	A: errata affected part
					B: 90nm errata free
					J: 300MHz
					K: 65nm
1412	x18	B2	1.5	36M	A:errata affected part
					B: 90nm, errata fix PCN061213
					J: 300MHz
					K: 65nm
1413	x18	B4	1.5	36M	A:errata affected part
					B: errata fix PCN071410
					J: 300MHz
					K: 65nm
1414	x36	B2	1.5	36M	A:errata affected part
					B: 90nm, errata fix PCN061213
					J: 300MHz
					K: 65nm
1415	x36	B4	1.5	36M	A:errata affected part
					B: errata fix PCN071410
					J: 300MHz
					K: 65nm
1425	x9	B2	1.5	36M	A:errata affected part
					B: errata fix PCN084656
					J: 300MHz
					K: 65nm

	1426	x9	B4	1.5	36M	A:errata affected part
						B: errata fix PCN084656
						J: 300MHz
						K: 65nm
	1510	x8	B2	1.5	64M	A: 90nm
						K: 65nm
	1511	x8	B4	1.5	64M	A: 90nm
						K: 65nm
1512	x18	B2	1.5	72M	no letter/default: errata affected	
					A: 90nm, errata fix PCN061213	
					J: 300MHz	
1513	x18	B4	1.5	72M	no letter/default: errata affected	
					A: 90nm, errata fix PCN061213	
					J: 300MHz	
1514	x36	B2	1.5	72M	no letter/default: errata affected	
					A: 90nm, errata fix PCN061213	
					J: 300MHz	
1515	x36	B4	1.5	72M	no letter/default: errata affected	
					A: 90nm, errata fix PCN061213	
					J: 300MHz	
1525	x9	B2	1.5	72M	no letter/default: errata affected	
					A: 90nm, errata fix PCN084656	
					J: 300MHz	
9-11	None	default				
	V18	1.8V Vdd				
	V25	2.5V Vdd				
	V33	3.3V Vdd				

12	-	Separator
13-15	100	Maximum operating frequency
	133	
	166	
	167	
	200	
	250	
	300	
	333	
16-17	400	
	A	TQFP
	AJ	TQFP, it has 2CE pins unlike 2 CE pins in A version
	BG	BGA
18	BZ	TSOP
	None	Leaded
19	X	Lead Free
	C	Commercial
	I	Industrial
20	A	Automotive
	None	Default
	T	Tape & Reel

Note: Please contact your local sales representative or visit www.cypress.com/support for parts not covered in this Part Number Dec

Legend:
SCD = Single Cycle Deselect, Pipelined
DCD = Double Cycle Deselect, Pipelined
FT = Flowthrough
TQFP = Thin Quad Flat Pack
FBGA = Fine Pitch Ball Grid Array
B2 = Burst of two
B4 = Burst of four