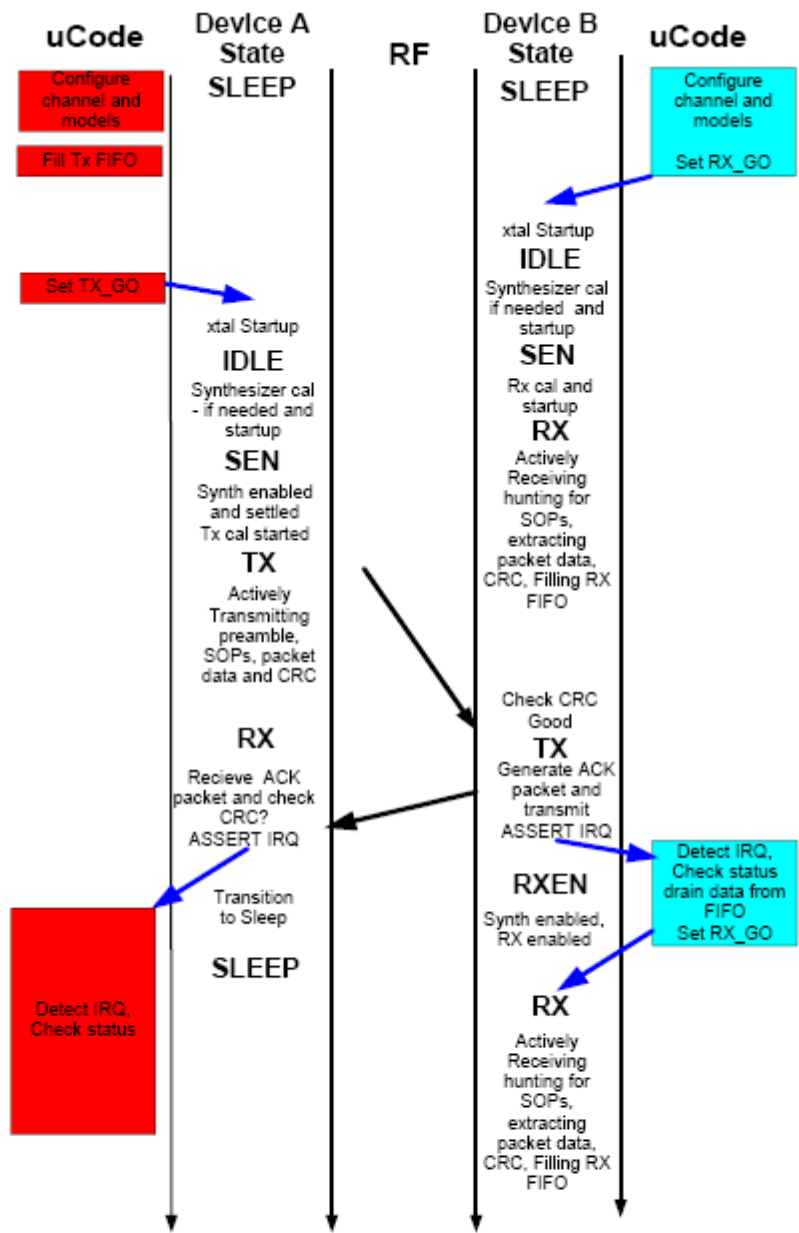


Supervisor State Machine



Supervisor State Diagram



State Transitions and Network timing