

NOR Flash part number decoder

Product family		S	Product / Interface	I/O architecture	Volt	Density (Mb, Gb)		Process node			Bus speed / Access speed			Pkg / Matl	Temp / Reliability
Serial	SEMPER™ Secure Safe and secure	S	35 = SPI 36 = HYPERBUS™ 38 = Octal	H High-performance serial	L 3.0 V	128 256 512	01G 02G 04G	T 45nm MB	DP	133MHz SDR / 66MHz DDR			BH BGA	I Industrial -40 to 85 °C	
	SEMPER™ Integrated functional safety		25 = SPI 26 = HYPERBUS™ 28 = Octal			166MHz SDR / 102MHz DDR				MH SOIC					
	HYPERFLASH™ High performance		26 = HYPERBUS™	S 65nm MB		166MHz DDR (3 V xSPI)					NH WSON				
	Standard SPI Broad portfolio		25 = SPI 70 = Stacked die 79 = Dual Quad SPI			F Serial (SPI)	L 65nm FG			200MHz DDR (1.8 V xSPI)			A Automotive Grade 3 -40 to 85 °C		
Parallel	Standard 16/8-bit wide Page mode	29 = Parallel 70 = Stacked die	G Page; No SRW	L 3.0 V	064 128 256			512 01G 02G	T 45nm MB	S 65nm MB	P 90nm MB	70 10 100 ns		80 11 110 ns	90 12 120 ns
	Legacy 8-bit wide Page and burst modes	29 = Parallel	A Std; No SRW	J Std; SRW	P Page; SRW	L 3.0 V S 1.8 V	008 016 032	064 128	J 110nm FG	55 55 ns	70 70 ns	80 80 ns	TF TSOP		

(see datasheet)